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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/773,522	02/02/2001	Paul A. Farrar	M4065.0392/P392	6270

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[REDACTED] EXAMINER

TALBOT, BRIAN K

ART UNIT	PAPER NUMBER
1762	

DATE MAILED: 04/01/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary	Application No.	Applicant(s)
	09/773,522	FARRAR ET AL.
	Examiner Brian K Talbot	Art Unit 1762

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on telephone restriction on 3/20/03.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-43 is/are pending in the application.
- 4a) Of the above claim(s) 1-34 is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 35-43 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 02 February 2001 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) Paper No(s). _____.
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) Notice of Informal Patent Application (PTO-152)
3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 4. 6) Other: _____

Election/Restrictions

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - I. Claims 1-10, drawn to a semiconductor die, classified in class 428, subclass 901.
 - II. Claims 11-34, drawn to a coating apparatus, classified in class 118, subclass 1+.
 - III. Claims 35-43, drawn to method of fabricating a flip chip semiconductor die, classified in class 438, subclass 1+.

The inventions are distinct, each from the other because of the following reasons:

2. Inventions III and II are related as process and apparatus for its practice. The inventions are distinct if it can be shown that either: (1) the process as claimed can be practiced by another materially different apparatus or by hand, or (2) the apparatus as claimed can be used to practice another and materially different process. (MPEP § 806.05(e)). In this case the apparatus as claimed can be used to practice another and materially different process other than fabricating flip chip semiconductors, such as welding two pieces of metal together.
3. Inventions III and I are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case the process as claimed can be used to make another and materially different product other than a flip chip semiconductor such as applying adhesive to a paper substrate.
4. Inventions II and I are related as apparatus and product made. The inventions in this relationship are distinct if either or both of the following can be shown: (1) that the apparatus as

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claimed is not an obvious apparatus for making the product and the apparatus can be used for making a different product or (2) that the product as claimed can be made by another and materially different apparatus (MPEP § 806.05(g)). In this case the product as claimed can be made by another and materially different apparatus such as by hand.

5. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

6. During a telephone conversation with Bill Powell on 3/20/03 a provisional election was made without traverse to prosecute the invention of Group III, claims 35-43. Affirmation of this election must be made by applicant in replying to this Office action. Claims 1-34 have been withdrawn from further consideration by the examiner, 37 CFR 1.142(b), as being drawn to a non-elected invention.

7. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

Claim Rejections - 35 USC § 102

8. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

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A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 35 and 36 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Hayes (5,377,902), Hayes et al. (5,229,016), Hieber (4,828,886) or Hayes et al. (MicroFab Technologies).

Hayes (5,377,902), Hayes et al. (5,229,016), Hieber (4,828,886) or Hayes et al. (MicroFab Technologies) all teach an ink jet printing head for depositing solder material for flip chip manufacture whereby the deposited solder has a diameter of less than 100 microns.

Claim 43 is rejected under 35 U.S.C. 102(b) as being clearly anticipated by Hayes (5,377,902).

Hayes (5,377,902) teaches an ink jet printing head for depositing solder material for flip chip manufacture whereby the deposited solder has a diameter of less than 100 microns.

Looking at Fig. 7, Hayes teaches forming multiple depositions of the solder in a single location (col. 6, line 37 – col. 7, line 8).

Claim Rejections - 35 USC § 103

9. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person

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having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 37-39 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hayes (5,377,902), Hayes et al. (5,229,016), Hieber (4,828,886) or Hayes et al. (MicroFab Technologies) in combination with Hayes (5,681,757).

Features described above are incorporated here.

Hayes (5,377,902), Hayes et al. (5,229,016), Hieber (4,828,886) or Hayes et al. (MicroFab Technologies) fail to teach the use of multiple heads to form the solder connections.

Hayes (5,681,757) teaches using multiple heads to form different solder coating or using multiple heads to form the solder connection with two different materials which are subsequently flowed together during the reflow process (col. 9, lines 29-55).

Claims 40-42 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hayes (5,377,902), Hayes et al. (5,229,016), Hieber (4,828,886) or Hayes et al. (MicroFab Technologies) in combination with Hayes (5,681,757).

Features described above are incorporated here.

Hayes (5,377,902), Hayes et al. (5,229,016), Hieber (4,828,886) or Hayes et al. (MicroFab Technologies) in combination with Hayes (5,681,757) fails to teach cleaning the pad prior to solder deposition and applying an adhesive and/or a passivation layer.

While the Examiner acknowledges that fact that the references are silent upon these limitations, it is the Examiner's position that these features are commonplace in the art and

would have been within the skill of a practitioner in the art to have been utilized with the well known expected advantages associated therewith.

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian K Talbot whose telephone number is (703) 305-3775. The examiner can normally be reached on Monday-Friday 6AM-3PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Shrive P Beck can be reached on (703) 308-2333. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9310 for regular communications and (703) 872-9311 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3775.

Brian K Talbot
Primary Examiner
Art Unit 1762

BKT
March 28, 2003